

Figure 1 displays 12 histograms showing the distribution of the number of non-zero elements in the vector x for different values of n (10, 20, 30, 40, 50, 60, 70, 80, 90, 100, 110, 120). The x-axis is labeled 'x' and ranges from 0 to 120. The y-axis is labeled 'count' and ranges from 0 to 100. The distributions are centered around 60 and become more spread out as n increases.

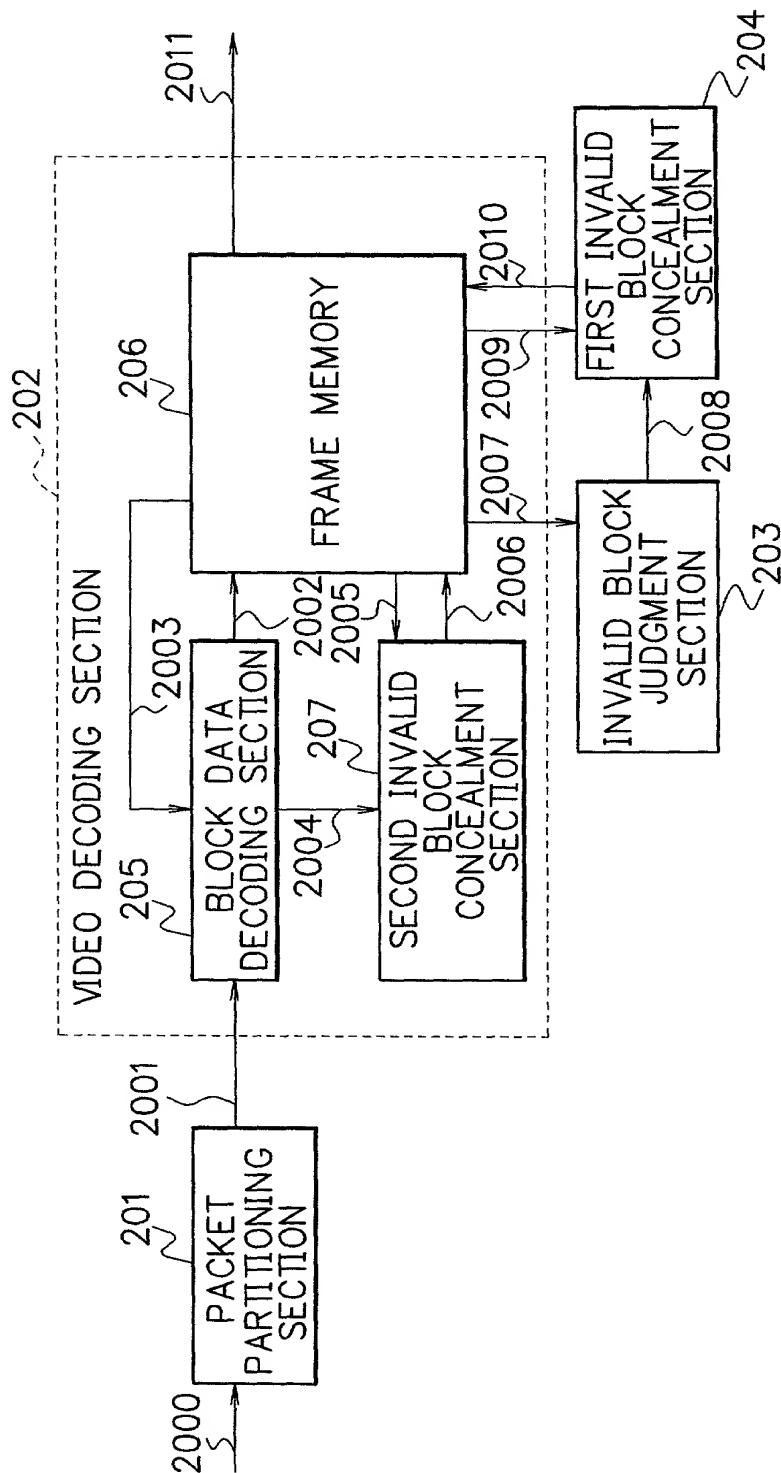
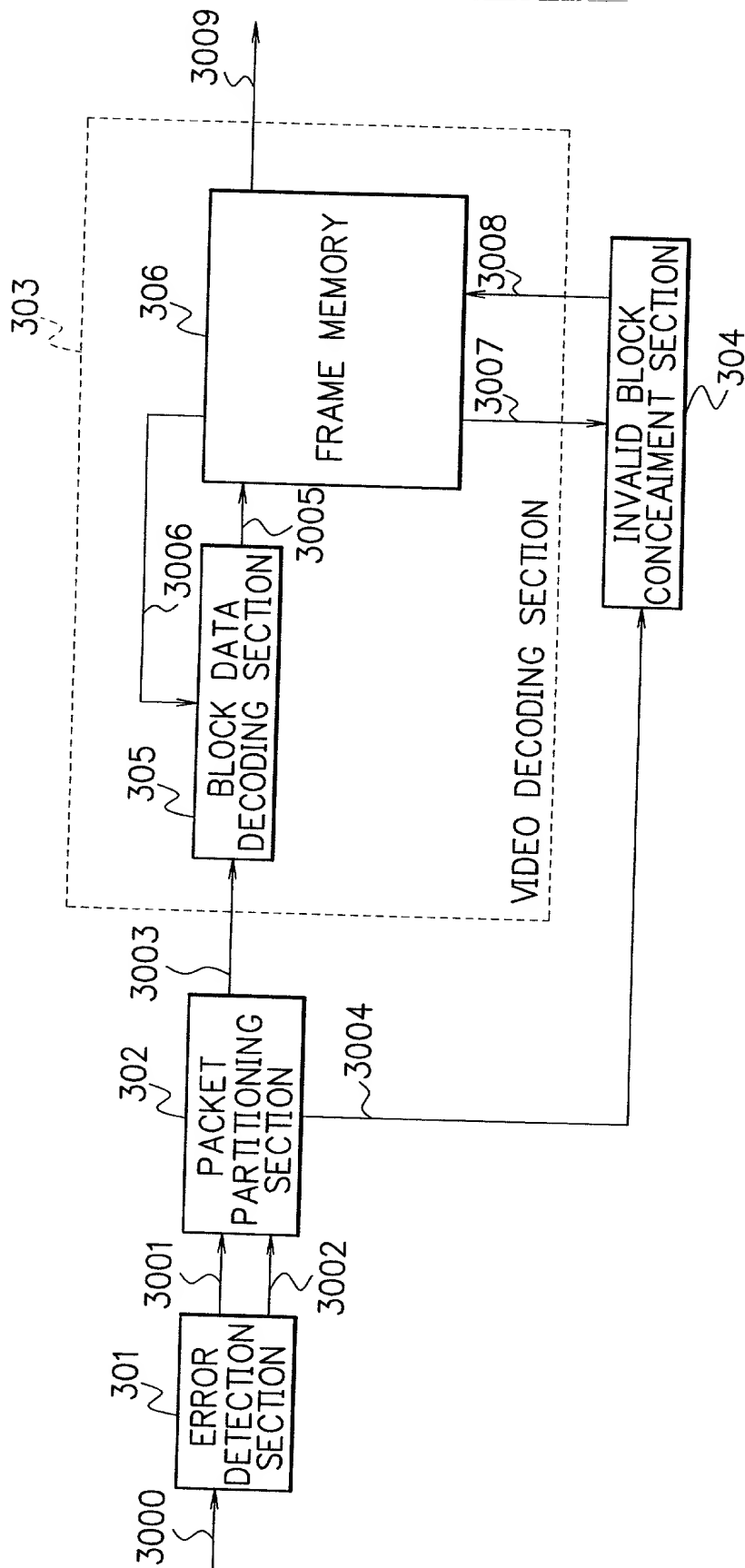


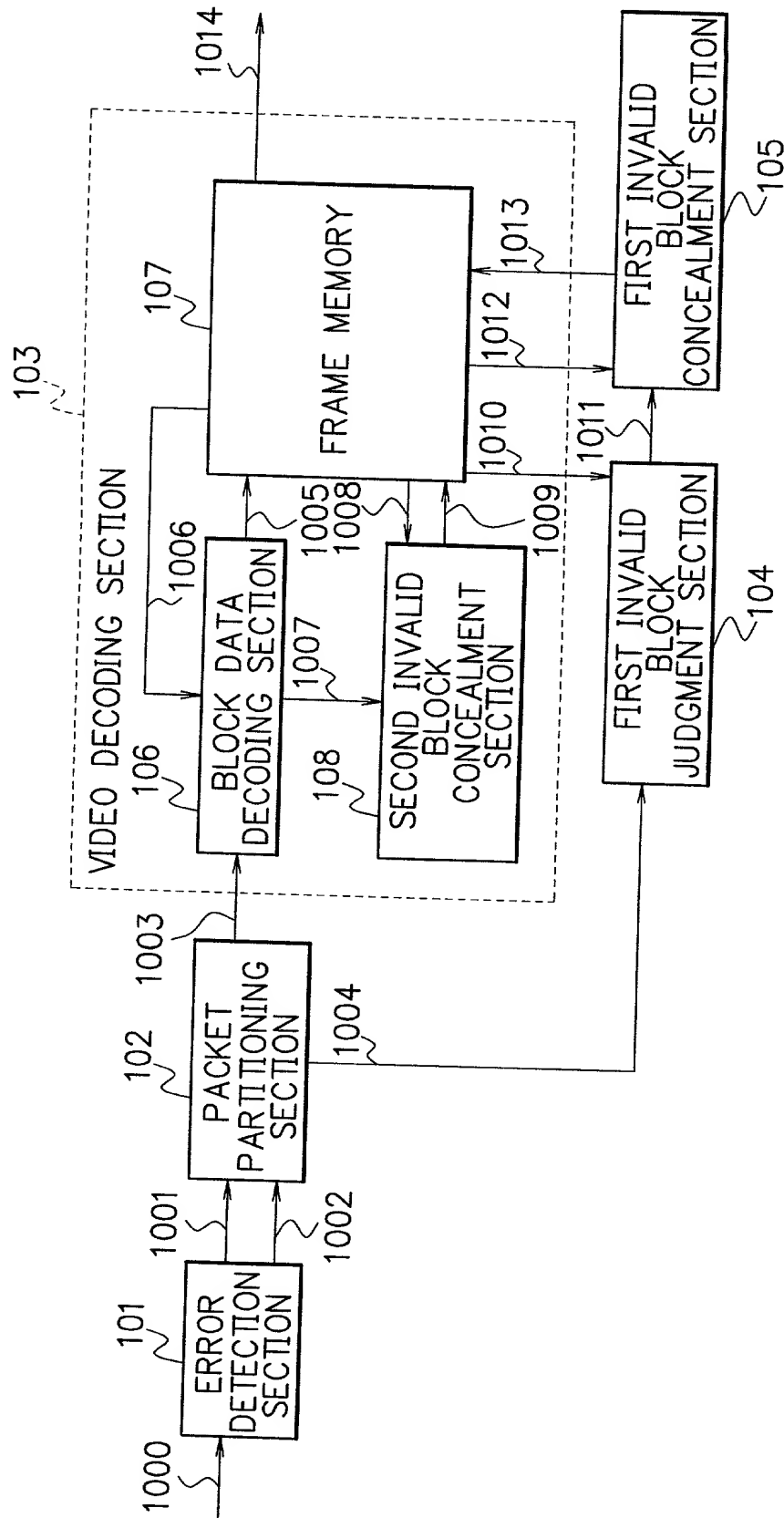
FIG. 2 (PRIOR ART)



title: DEVICE, METHOD AND RECORD
MEDIUM FOR VIDEO DECODING CAPABLE
OF CONDUCTING ERROR DETECTION
PROCESS AND CONCEALMENT PROCESS
EFFECTIVELY

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FIG. 3



[illegible]

FIG. 4

[illegible]

FIG. 5

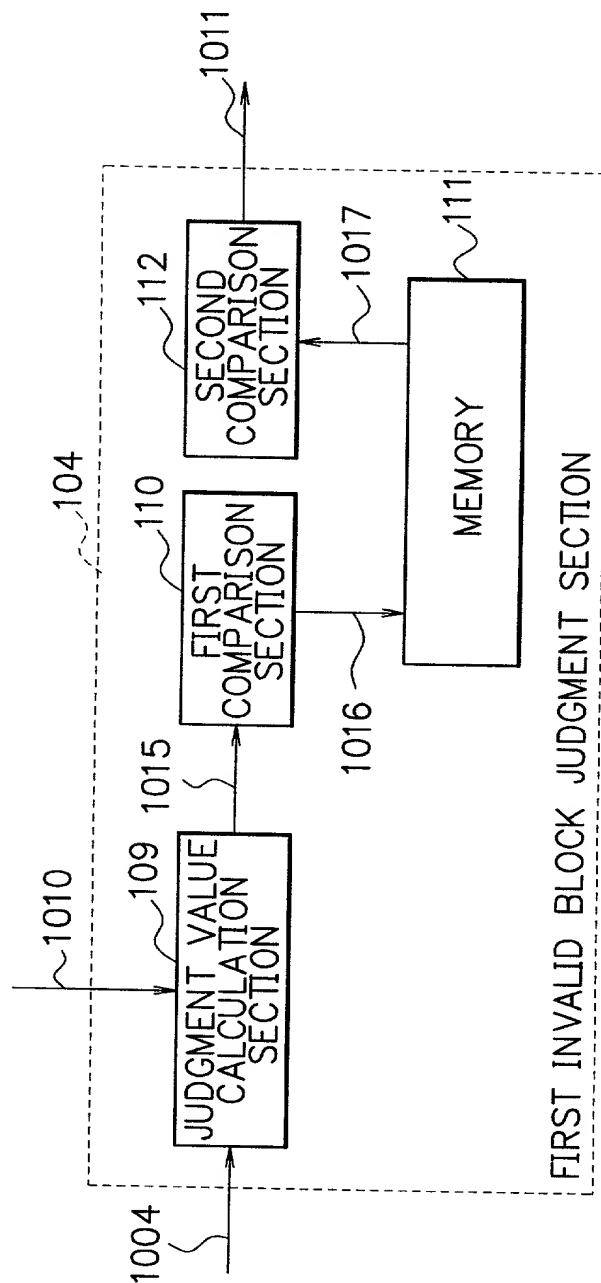
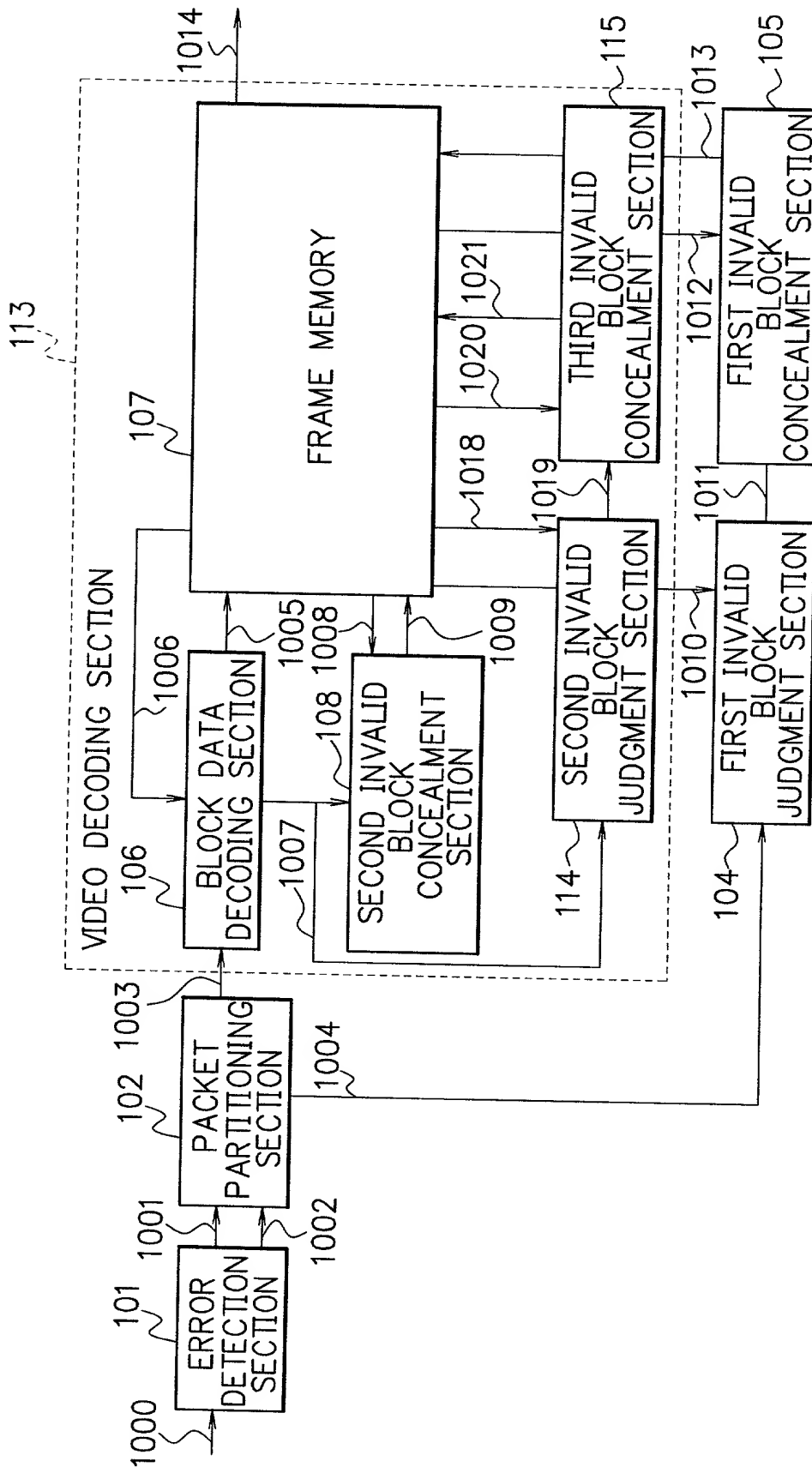


FIG. 6



The diagram illustrates a video decoding system (113) with the following components and signal flow:

- Input:** Signal 1000 enters the **ERROR DETECTION SECTION** (101).
- ERROR DETECTION SECTION (101):** Outputs signal 1001 to the **PACKET PARTITIONING SECTION** (102) and signal 1002 to the **SECOND INVALID BLOCK JUDGMENT SECTION** (114).
- PACKET PARTITIONING SECTION (102):** Outputs signal 1003 to the **BLOCK DATA DECODING SECTION** (106).
- BLOCK DATA DECODING SECTION (106):** Outputs signal 1006 to **FRAME MEMORY** (107) and signal 1007 to the **SECOND INVALID BLOCK CONCEALMENT SECTION** (109).
- FRAME MEMORY (107):** Outputs signal 1008 to the **SECOND INVALID BLOCK CONCEALMENT SECTION** (109) and signal 1010 to the **THIRD INVALID BLOCK CONCEALMENT SECTION** (115).
- SECOND INVALID BLOCK CONCEALMENT SECTION (109):** Receives signals 1007 and 1008, and outputs signal 1009 to the **SECOND INVALID BLOCK JUDGMENT SECTION** (114).
- SECOND INVALID BLOCK JUDGMENT SECTION (114):** Receives signals 1002 and 1009, and outputs signal 1019 to the **THIRD INVALID BLOCK CONCEALMENT SECTION** (115).
- THIRD INVALID BLOCK CONCEALMENT SECTION (115):** Receives signals 1010 and 1019, and outputs signal 1020 to **FRAME MEMORY** (107) and signal 1021 to the final output.
- Output:** Signal 1014 is the final output of the system.